

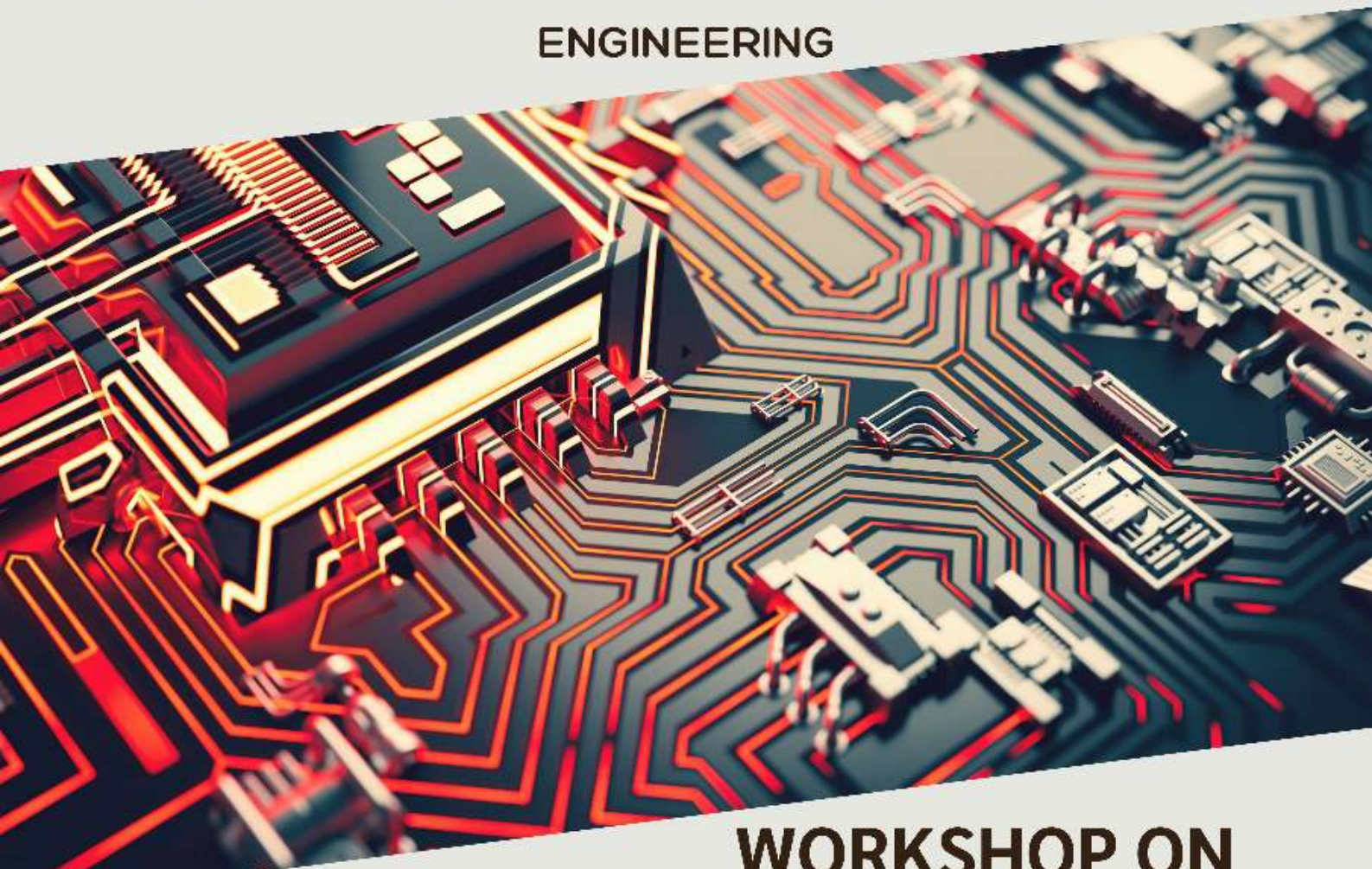


**RSET**  
RAJAGIRI SCHOOL OF  
ENGINEERING & TECHNOLOGY  
(AUTONOMOUS)



ELECTRONAUTS

DEPARTMENT OF ELECTRONICS & COMMUNICATION  
ENGINEERING



## WORKSHOP ON PCB DESIGN WITH KICAD



**Mr. Jerin Jose**  
(2020-2024 ECE BATCH)



28th September 2024



11:30 am - 4:30 pm



Signal Processing Lab



NISY IGNATIUS +91 8281227834