



## NOTICE

Notice No: RSET/PR/N/8092

Date: 22-Jul-2024

### **Two Day Skill Development Programme on “FPGA based Digital Design”**

Department of Electronics & Communication is organizing a two-day hands-on skill development program on “FPGA based Digital Design” for the faculty members of the department of Electronics and Communication on 23<sup>rd</sup> & 24<sup>th</sup> July 2024.

Resource Person: Prof. Dr. Jobin K Antony,

**Department of Electronics and Communication and Engineering**

**Rajagiri School of Engineering & Technology (Autonomous), Kakkanad**

Time : 9:00 am to 4:00 pm

Venue : Signals and Systems Lab

Interested faculty members may please register in the following link: <https://forms.gle/5Hb9WqbNWSHBZ4wE9>

Co-ordinator

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