RSET
RAJAGIRI SCHOOL OF ENGINEERING & TECHNOLOGY (AUTONOMOUS)

N O T I C E

Notice No: RSET/PL/N/6419
Date : 10-Jun-2022

Workshop on Introduction to RISC-V architecture

Department of Electronics and Communication in association with C-DAC, Thiruvananthapuram is conducting a workshop on Introduction to RISC-V architecture and India's indigenous microcontroller (VEGA) development board on 10th June 2022.

Venue: Multimedia hall, Shockley lab

PRINCIPAL

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