



RSET/Exam 1/24/215

NOTIFICATION

Sub: Registration and Schedule of M.Tech. Degree First Semester (2024 admission)

End Semester Examination

M.Tech. Degree First Semester End Semester Examination (2024 admission) is scheduled to be held from **December 11, 2024 to December 20, 2024**.

Date	Duration	Course Code	Course Name	M. Tech. programmes
09/12/2024 Monday	9.30 am – 12.30 pm	202107/MA100A	Mathematical Foundations for Machine Learning	AIML
		202902/MA100A	Mathematical Foundations	CSE/CSIS
		202108/MA100A	Mathematical Foundations for Data Science	DS
		202105/EC100A	Advanced Microcontrollers & Real Time Operating Systems	VAES
11/12/2024 Wednesday	9.30 am – 12.30 pm	202902/CO100B	Advanced Data Structures and Algorithms	AIML/CSE
		202102/CS100B	Algorithms and Complexity	CSIS
		202108/IT105D	Advanced Data Structures and Algorithms	DS
		202105/EC100B	CMOS VLSI Design	VAES
13/12/2024 Friday	9.30 am – 12.30 pm	202802/CO100C	Machine Learning Algorithms	AIML/CSIS
		202106/CS101D	Machine Learning Algorithms	CSE
		202108/IT100B	Data Science	DS
		202105/EC100C	FPGA Based System Design	VAES
16/12/2024 Monday	9.30 am – 12.30 pm	202107/AD103D	Advanced Python for AI & ML	AIML
		202903/CO101D	Computer Vision	AIML
		202902/CO100C	Cloud Computing	CSE/DS
		202102/CS102D	Computational Essentials for Data Science	CSIS
18/12/2024 Wednesday	9.30 am – 12.30 pm	202105/EC101D	Advanced Digital System Design	VAES
		202904/CO101E	Computational Linguistics	AIML/CSE/CSIS/DS
		202105/EC101E	Functional Verification with System Verilog	VAES

Exam registration can be done in the REXA portal from 10 am on 02 December to 4 pm on 04 December 2024. Examination fee to be paid: ₹ 1750 (5×250 + 500).

Late registration: after 4 pm on 04 December to 4 pm on 05 December 2024.

Late registration fee: ₹ 3000 (5×500 + 500).

November 18, 2024

Controller of Examinations